

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S72	0	S71 and vitual	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 13:58
S71	20	(US-20040168105-\$ or US-20050102565-\$ or US-20030005380-\$).did. or (US-5412671-\$ or US-6907548-\$ or US-6857083-\$ or US-6367032-\$ or US-5835776-\$ or US-6434712-\$ or US-6134675-\$ or US-6553530-\$ or US-5202975-\$ or US-6360333-\$ or US-5819088-\$ or US-6408377-\$ or US-6550020-\$ or US-6983398-\$ or US-6640313-\$ or US-6625749-\$ or US-7065681-\$).did.	US-PGPUB; USPAT	OR	ON	2007/02/02 13:58
S70	3	virtual with swapping with core	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 13:58
S74	5	S71 and identifier	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 14:00
S73	19	S71 and number	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 14:00
S75	3	S71 and number with (switch\$3 or swap\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 14:15
S80	11	S78 with diagnosi\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 15:21
S79	259	S78 with test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 15:21

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S78	7316	multi-core or (multiple adj core)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 15:21
S77	927	(714/30).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 15:21
S76	1885	(714/25).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 15:21
S82	761	S81 with (test\$4 or diagnos\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 15:22
S81	17030	(multi-core or (multiple adj core) or (CPU adj core) or (processor adj core))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/02 15:22

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frequency of **context switch** is limited less than 10kHz. order. We applied multi thread

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the smaller **context switch** extended array. From CPU **core** point of view there are ...Engine with a **Multi-Context/multi-mode Unified-Cell Architecture**. ...[www.springerlink.com/index/13V482EP792JNUY6.pdf](http://www.springerlink.com/index/13V482EP792JNUY6.pdf)Result Page:    [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#)    [Next](#)Download [Google Pack](#): free essential software for your PC[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)[Try your query on the entire web](#)[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

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accesses during **context switches**. The whole multi-processor. system, including software stack and ... multi-processor systems. Each processing **core** in a SMP ...  
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spent by processes waiting on communication events in the multi-programming envi-.  
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new multicore chipset, the software architecture had to be properly rearranged, ... the scheduler policy, the average time spent in a **context switch**, etc. ...

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In general, **testing** multi-threaded code. is an extremely hard task. Hand-generated

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appropriate chunk of AL in this **context**. This sub-area relates to many other parts. of  
our field, but its **core** is, in my view at any rate, ...  
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